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PTO-1449			Application No.	A	Applicant(s)							
	_	- (4)	f 70.45	CHAPTER TO THE PARTY OF THE PAR		09/997,786		uncan M. Wa	alker et al			
Information Disclessific Citation					n	Docket Number		roup Art Unit	Filing Date			
		in an Appl	icati	ion				2800				
						017575.0551		080	11/30/2001		<del></del>	
	U.S. PATENT DOCUMENTS											
		DOCUMENT NO. DATE			NAME		CLASS SUBCLASS		FILING DATE			
1	A			P	opovic et al		324			06/07/1983		
2	В				$\frac{1}{N}$	eedham et al.		324	763	12/29/1994		
35	4	3,370,034		10,23,1330	1.					<u> </u>		
FOREIGN PATENT DOCUMENTS												
									TRANSL			
		DOCUMENT	NO.	DATE		COUNTRY		CLASS	SUBCLASS	YES	NO	
	С		_									
	D										1	
	1			<u> </u>		ION-PATENT DOCUM	IENTS	<u>.                                    </u>				
	NON-PATENT DOCUMENTS											
	E	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)									DATE Aug. 1992	
21	-	J. Lin and M. Milkovic, "Performance Limitations of Stochastic Sensors," Midwest Symposium on Circuits and Systems									Aug. 1372	
	F											
1		the Relative Ef	fectiv	eness of Fund	tiona	il, Scan, IDDq and De	lay-fault	Testing, IEEF	Int'l ASIC	ľ		
		Conference							. 531	<del> </del>		
	G	J. Rius and J. Figueras, "Dynamic Characterization of Built-In Current Sensors Based on PN  Dec. 1996										
1 1		Junctions: Analysis and Experiments," <u>Journal of Electronic Testing: Theory and Applications</u> , Vol. 9, No. 3										
$\Box$	Н	J.M. Soden and C.F Hawkins, "I <sub>DDQ</sub> Testing and Defect Classes – A Tutorial," <u>IEEE Custom</u> 1995										
		Integrated Circuits Conference										
		K.M. Wallquist Conference	t, "On	the Effect of	IssQ	Testing in Reducing E	arly Fail	lure Rate," IEI	EE Int'i Test	1995		
	J		T. Sc	o. "Burn-In	Elimi	nation of a High Volu	me Micr	oprocessor Us	ing Ippo."	1996	<del></del>	
		1EEE Int'l Test Conference										
	К	T.W. Williams, R. Kapur, M.R. Mercer, R.H. Dennard and W. Maly, "Iddq Test: Sensitivity 1996										
		Analysis of Scaling, IEEE Int'l Test Conference  D.M.H. Walker, "Requirements for Practical IDDQ Testing of Deep Submicron Circuits," IEEE Int'l April 2000										
	L.	D.M.H. Walker Workshop on D	r, Ke Defect	quirements to Based Traini	or Pra Ng	ictical IDDQ Lesting of	Deeb 20	omicron Circi	no, <u>ieee mu</u>	Αμετι 20	00	
	M	P.C. Maxwell,	R.C.	Aitken, K.R.	Kolli	z and A.C. Brown, "I	DDQ and	d AC Scan: T	he War Against	1996		
		Unmodelled De	fects,	" IEEE Int'l	<u>Test</u>	Conference				27 10	<u> </u>	
H	N	T. Meneghini a	nd D.	Josephson, "	וססס יי זיביו	Testing of a 180MHz EE Int'l Workshop on	ያት የ <b>ለ-</b> ፤	KISC Micropro	ocessor with	Nov. 19	9 /	
$\vdash \vdash \vdash$	0	P. Nigh. D. Val	lett A	Patel and I	Wri	eht, "Failure Analysis	of Timi	ng and IDDo-c	only Failures	1998		
		from the SEMATECH Test Methods Experiment," IEEE Int'l Test Conference										
	P	T.A. Unni and D.M.H. Walker, "Model-Based IDDQ Pass/Fail Limit Setting, IEEE Int'l Workshop Nov. 1998									98	
┝╼╂╌┨	Q	on IDDQ Testing  A.E. Gattiker and W. Maly, "Current Signatures: Application," <u>IEEE Int'l Test Conference</u> 1997										
	•	A.E. Gattikei and W. Mary, Current Signatures: Application, IEEE Int 1 less Contelence										
V	R C. Thibeault, "On the Comparison of Δ I <sub>DDQ</sub> and I <sub>DDQ</sub> Testing," <u>IEEE VLSI Test Symposium</u>									April 1999		
EXA	MINI	ER				DATE	CONSIL	ERED .		<del></del>		
2/28/05												
		Jun 1	<u> </u>	7						\		
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PTO	D-144	9	-1019	Application	No.	Applicant(s)				
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		in an Applicati	ion				Filing Date	•		
				017575.0	551	2829	11/30/2001			
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	l	DOCUMENT NO.	DATE	NA	ME	CLASS	SUBCLASS	FILING DATE		
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	L		L				<del></del>			
				FORFIGN PATE	NT DOCUME	NTS				
	Γ			TOTAL ON THE	REIGN PATENT DOCUMENTS			TRANSLATION		
		DOCUMENT NO.	COU	NTRY	CLASS	SUBCLASS	YES	NO		
	С									
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				NON-PATEN	DOCUMENT	rs				
		2001	MENT //	d' - A - Ab <b>T</b> 'Al	0	10 4		DA		
-1	D	DOCUMENT (Including Author, Title, Source, and Pertinent Pages)  C. Thibeault, "An Histogram Based Procedure for Current Testing of Active Defects," IEEE Int'l								
2	-	Test Conference	stogram base	u Frocedule for C	mient resum	g of Active Defect	s, <u>iece in i</u>	1999		
1	Ε	A.C. Miller, "IDDQ"	Testing in De	ep Submicron Int	egrated Circu	its." IEEE Int'l Te	st Conference	1999		
T	F	P. Maxwell, P. O'Neill, R. Aitken, R. Dudley, N. Jaarsma, M. Quach and D. Wiseman, "Current 1999								
		Ratios: A Self-Scaling Technique for Production IDDQ Testing," IEEE Int'l Test Conference								
	G	S. Jandhyala, H. Balachandran, S. Menon and A. Jayasumana, "Clustering Based Identification of Faulty ICs Using I <sub>DDQ</sub> Tests," IEEE Int'l Workshop on IDDQ Testing								
+	Н	S. Jandhyala, H. Bala	chandran A	P Tavasumana "(	Unitering Rad	sed Techniques for	· Inna Testing "	1999		
	• •	IEEE Int'l Test Confe		. Jayasumana, V	olusioning Da	sea reciniques foi	ippo resting,	1,777		
	1	S. Hentschke, S. Roh		ifschneider, "Sto	hastic Magne	tic Field Micro-Se	nsor," IEEE	1996		
$\bot$		Int'l ASIC Conference	e					1997		
	J	J.P.M. van Lammeren, "ICCQ: a Test Method for Analogue VLSI Based on Current Monitoring,"								
+	K	IEEE Int'l Workshop on IDDQ Testing								
	~	K. Nose and T. Sakurai, "Micro IDDQ Test Using Lorentz Force MOSFET's, IEEE Symposium 199								
+ 1	L	on VLSI Technololgy  F.J. Kub and C.S. Scott, "Multiple-Gate Split-Drain MOSFET Magnetic-Field Sensing Device and 1992								
		Amplifier," Internation	nal Electron	Devices Meeting			_			
	M	HM. Yang, YC. H	uang, TF. Le	ei, CL. Lee and				1996		
44	<del></del> _	Sensor with Thin Oxi						<u> </u>		
	N	J.W.A. von Kluge and						Jan. 1999	9	
		Including Secondary Effects Using a Continuous Description of the Geometric Correction Factor G," IEEE Transactions on Electron Devices, Vol. 46, No. 1								
-{-{	0	S. Hentschke, "Digital Stochastic Magnetic-Field Detection," Sensors and Actuators, Vol. A57 1996								
		1970								
$\top$	Р	H.J.M. Veendrick, "The Behavior of Flip-Flops Used as Synchronizers and Prediction of Their								
4-1	Failure Rate," IEEE Journal of Solid-State Circuits, Vol. SC-15, No. 2									
9	A.D. Singh, "Experiments with an On-Chip IDDQ Current Sensor for VLSI Testing," IEEE Int'!									
Workshop on IDDQ Testing   DATE CONSIDERED / /										
Jun 1/2   BATE CONSIDERED 3/3/05										
		Juny	ra			5/1		,		
EXA	MINE	R: Initial if citation c	onsidered, w	hether or not cit	ation is in co	nformance with N	1PEP § 609. D	raw line t	hrough	
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## U.S. Patent and Trademark Office